

P-Channel Trench MOSFET

Description

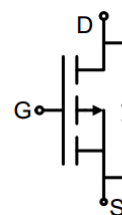
The G11S uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

General Features

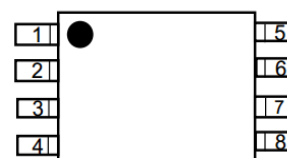
- V_{DS} -20V
- I_D (at $V_{GS} = -10V$) -11A
- $R_{DS(ON)}$ (at $V_{GS} = -4.5V$) < 18.4m Ω
- $R_{DS(ON)}$ (at $V_{GS} = -2.5V$) < 24.5m Ω
- 100% Avalanche Tested
- RoHS Compliant

Application

- Power switch
- DC/DC converters



Schematic diagram



Marking and pin assignment



SOP-8

Device	Package	Marking	Packaging
G11S	SOP-8	G11	4000pcs/Reel

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-20	V
Continuous Drain Current	I_D	-11	A
Pulsed Drain Current (note1)	I_{DM}	-44	A
Gate-Source Voltage	V_{GS}	± 12	V
Power Dissipation	P_D	3.3	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ\text{C}$

Thermal Resistance

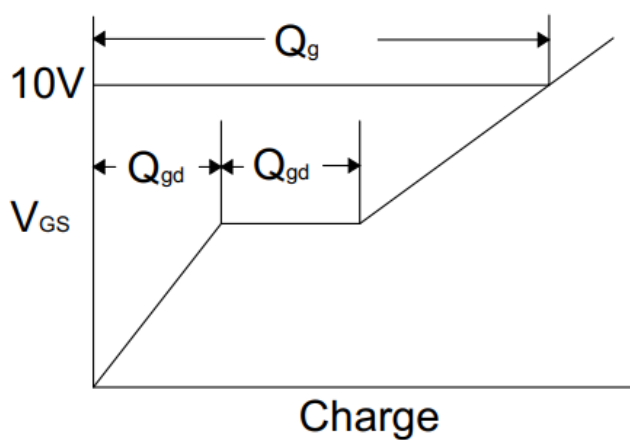
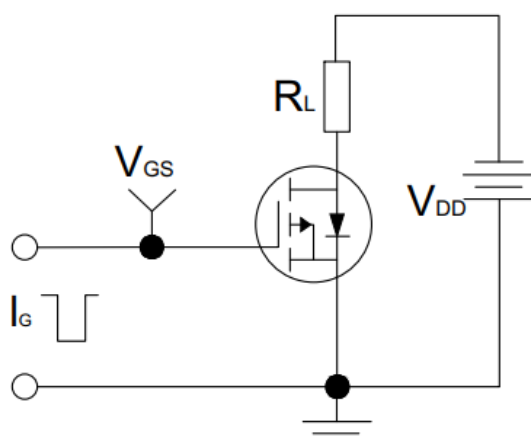
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	38	$^\circ\text{C/W}$

Specifications T _J = 25°C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-20	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V, T _J = 25°C	--	--	-1	μA
Gate-Source Leakage	I _{GSS}	V _{GS} = ±12V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-0.4	-0.65	-1.1	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = -4.5V, I _D = -1A	--	10	18.4	mΩ
		V _{GS} = -2.5V, I _D = -1A	--	12	24.5	
Forward Transconductance	g _{FS}	V _{DS} =-5V,I _D =-1A	--	90	--	S
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = -10V, f = 1.0MHz	--	2225	--	pF
Output Capacitance	C _{oss}		--	412	--	
Reverse Transfer Capacitance	C _{rss}		--	281	--	
Total Gate Charge	Q _g	V _{DD} = -10V, I _D = -1A, V _{GS} = -10V	--	47	--	nC
Gate-Source Charge	Q _{gs}		--	5	--	
Gate-Drain Charge	Q _{gd}		--	10	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = -10V, I _D = -1A, R _G = 3Ω	--	3.5	--	ns
Turn-on Rise Time	t _r		--	6	--	
Turn-off Delay Time	t _{d(off)}		--	67	--	
Turn-off Fall Time	t _f		--	22	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25°C	--	--	-11	A
Pulsed Diode Forward Current	I _{SM}		--	--	-44	
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = -1A, V _{GS} = 0V	--	--	1.2	V

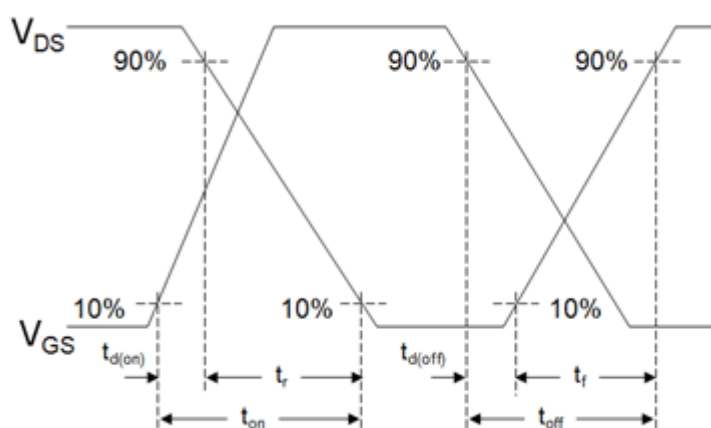
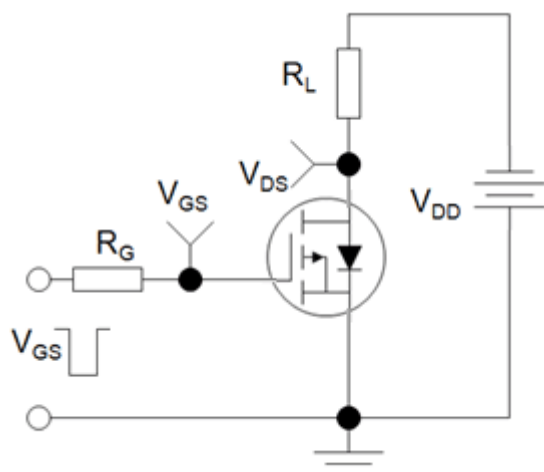
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical R_G

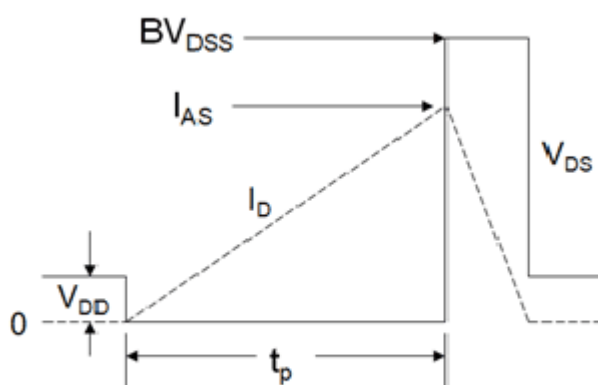
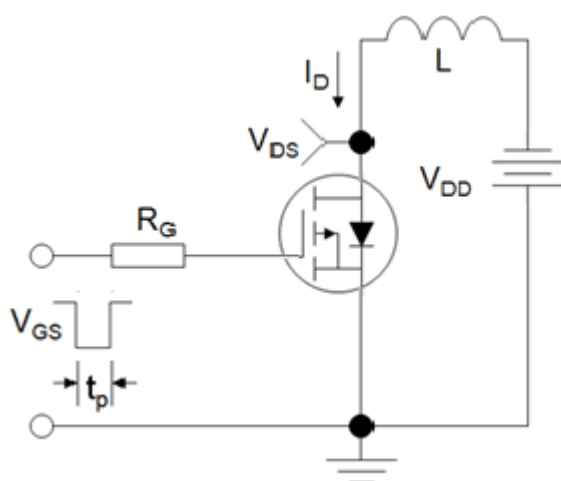
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

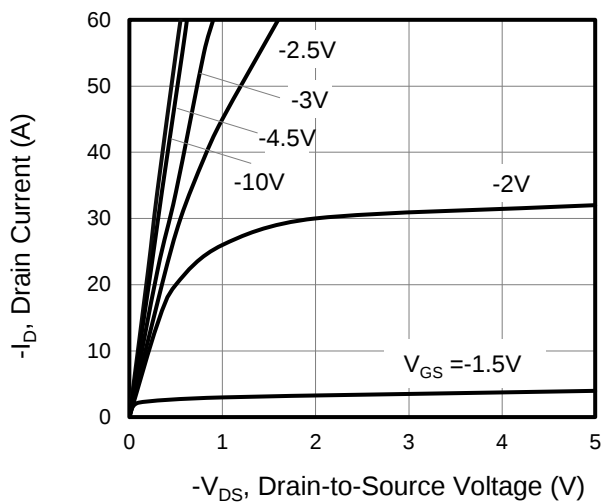


Figure 2. Transfer Characteristics

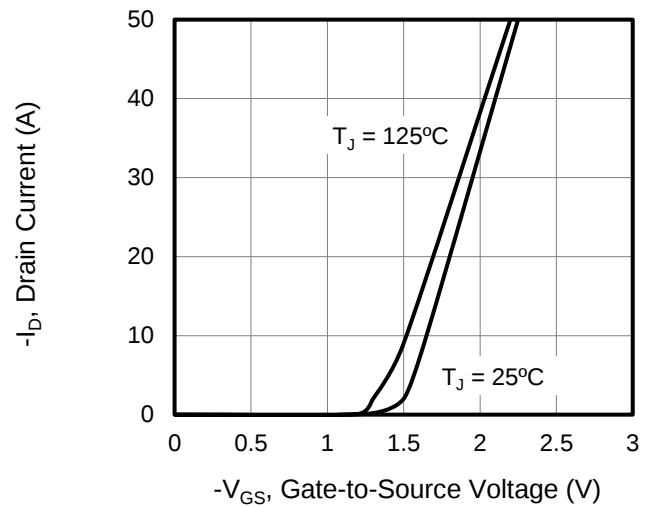


Figure 3. $R_{DS(on)}$ -Drain Current

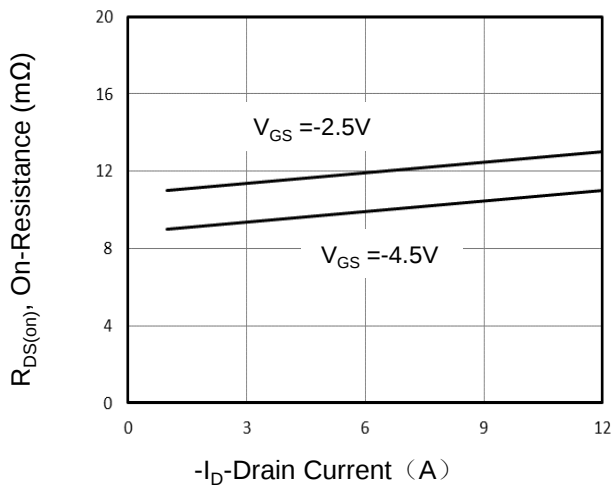


Figure 4. Gate Charge

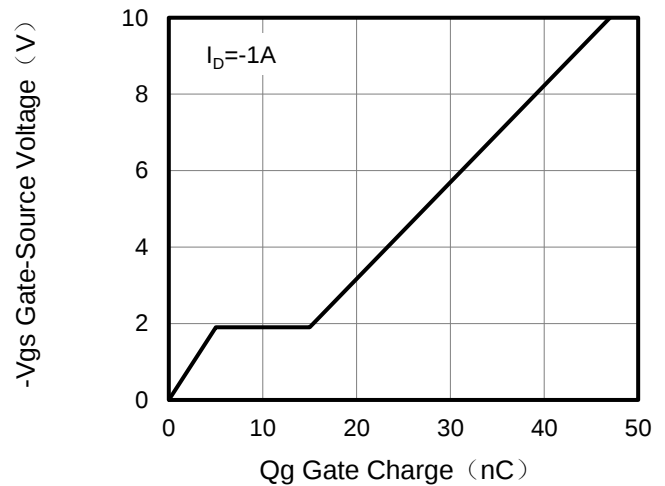


Figure 5. Capacitance vs V_{DS}

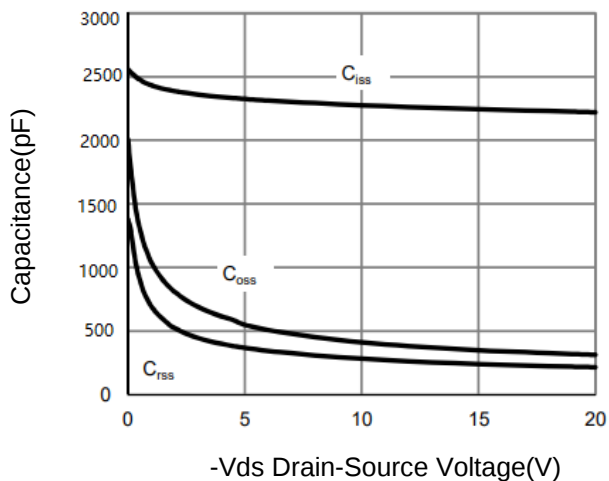
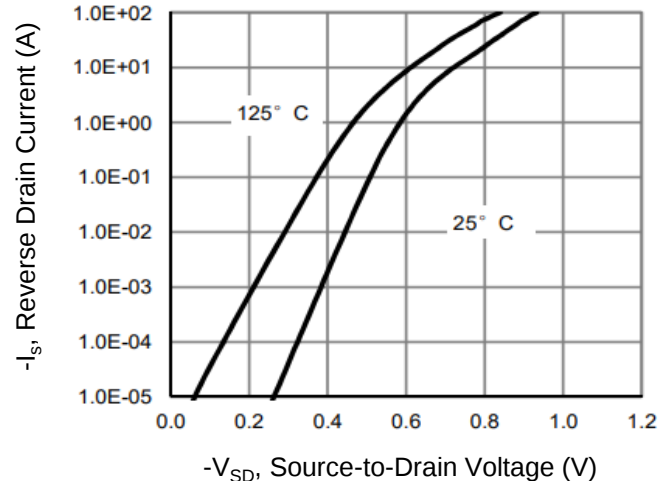


Figure 6. Source-Drain Diode Forward



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Figure 7. Drain-Source On-Resistance

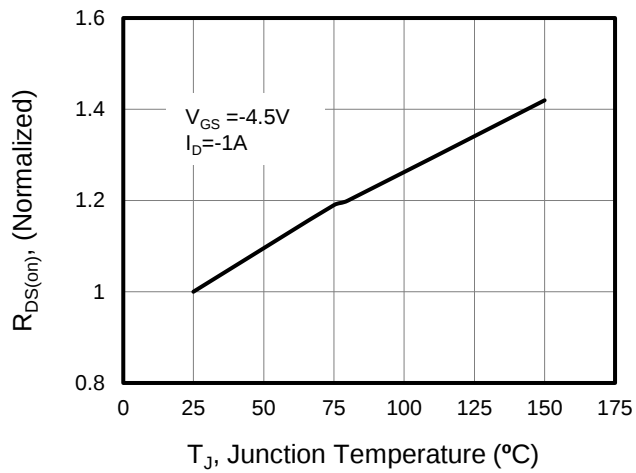
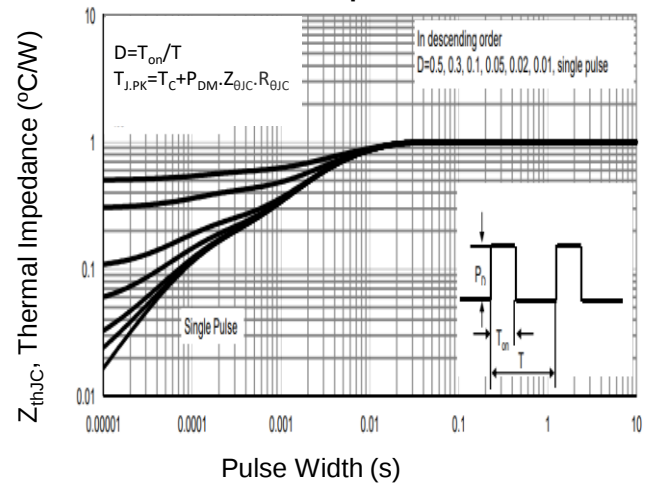
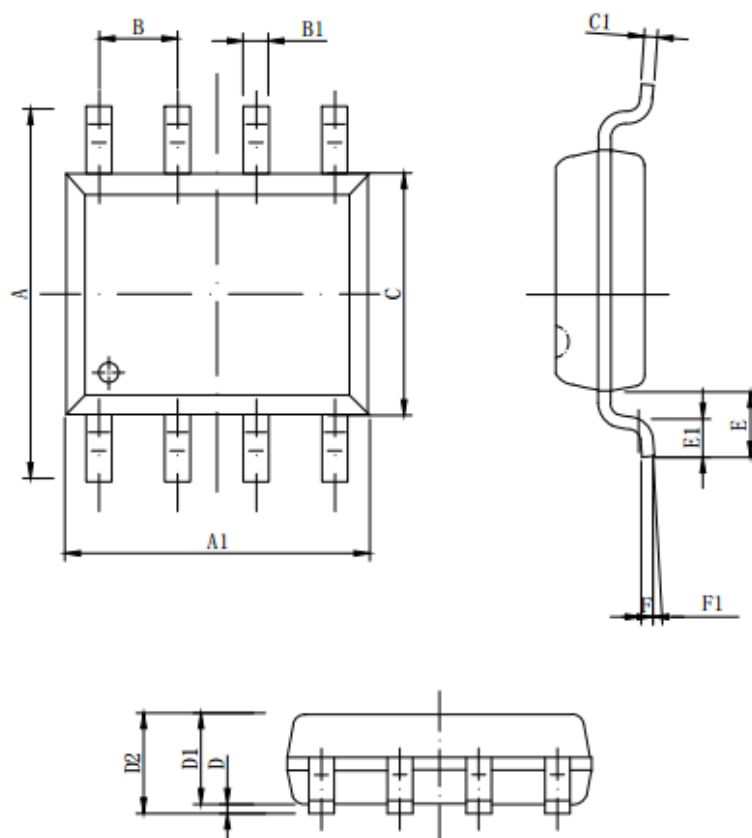


Figure 8. Normalized Maximum Transient Thermal Impedance



SOP-8 Package Information



Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	5.800	6.000	6.200
A1	4.800	4.900	5.000
B	1.270BSC		
B1	0.35 ^{8x}	0.40 ^{8x}	0.45 ^{8x}
C	3.780	3.880	3.980
C1	--	0.203	0.253
D	0.050	0.150	0.250
D1	1.350	1.450	1.550
D2	1.500	1.600	1.700
D2	1.500	1.600	1.700
E	1.060REF		
E1	0.400	0.700	0.100
F	0.250BSC		
F1	2°	4°	6°